

## Using DoCD debug software with Keil OMF files

DoCD debugger fully supports OMF-51 and OMF-251 files produced by Keil software. To enable all debugging options inside Keil uVision2 software the following steps should be done:

1. Create a project with Keil-uVision2 software or open any existing project.



2. Select "DoCD", use right click of mouse and choose from pop-up menu "Options for Target 'DoCD' ". <u>Please note that you may have a different name of Target than 'DoCD'.</u>



3. You should see the following window box. Select the "Output" tab. Check in the "Debug Information" and "Browse Information".

All trademarks mentioned in this document are trademarks of their respective owners.

http://www.DigitalCoreDesign.com http://www.dcd.pl

Options for Target 'DoCD'	<u>?</u> ×				
Device Target Output Listing C51 A51 BL51 Locate BL51 Misc Debug Utilities					
Select Folder for Objects Name of Executable: DoCD.OMF					
Create Executable: .\DoCD.OMF					
C Debug Information					
Create HEX File HEX Format:					
C Create Library: .\DoCD.OMF.LIB					
After Make					
☑ Beep When Complete ☑ Start Debugging					
Run User Program #1:     Browse					
Run User Program #2: Browse					
OK Anuluj Defaults					

Press OK button to save your changes and recompile the design. All debug information are stored in file named "DoCD.OMF". <u>Please note that you may have different name of executable file.</u>

4. The executable file should be used with DoCD while project is being created using DoCD Wizard.

🗞 Sy:	stem on	Chip Deb	ugger			
Projec	t View	Debug F	Peripherals	Window	Help	
	<b>i</b> -	RST ∔	11	())		P'
	Bank 0	Bank 1	Bank 2	Bank3	New Project Wizard: Program memory source file	
RO	0x00	0x00	0x00	0x00	Please specify your code file:	I
R1	0x00	0x00	0x00	0x00		I
R2	0x00	0x00	0x00	0x00	Program memory file:	I
R3	0x00	0x00	0x00	0x00		I
R4	0x00	0x00	0x00	0x00	File type:	I
R5	0x00	0x00	0x00	0x00	© Kel OMF-51	I
R6	0x00	0x00	0x00	0x00	C Intel HEX	I
R7	0x00	0x00	0x00	0x00	C Binary	I
CPU registers						
PC 0x0000						I
SP 0x00						I
DPX0 0x00 DPTR0 0x0000			TRO 0x0	000	< Back Finish Cancel	
DPX1 0x00 DPTR1 0x0000			TR1 0x0	000		

All trademarks mentioned in this document are trademarks of their respective owners.

http://www.DigitalCoreDesign.com http://www.dcd.pl

Copyright 1999-2004 DCD - Digital Core Design. All Rights Reserved.