

# ADSP-219x Architecture Overview

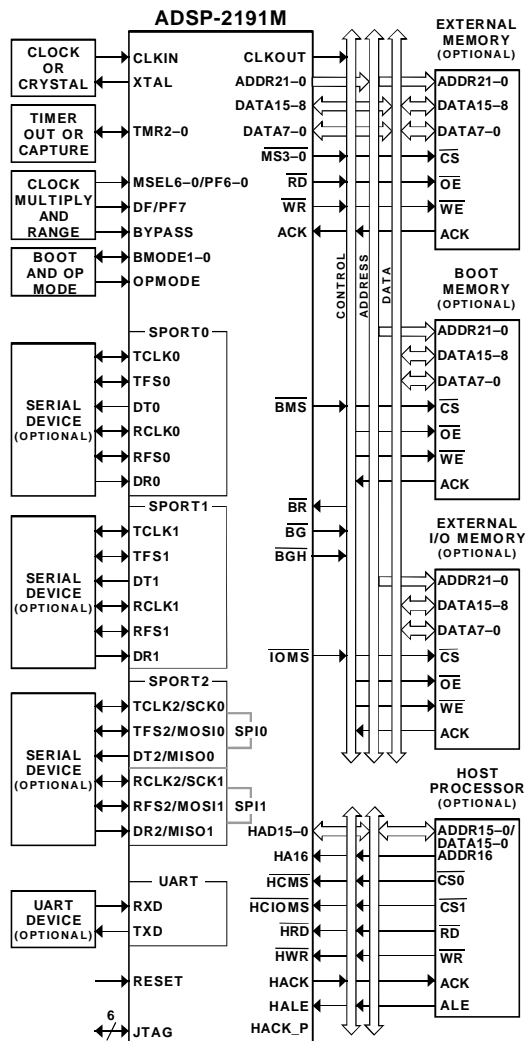


Figure 1-2. ADSP-219x/ADSP-2191 DSP Block Diagram

which include the UART port, serial ports, SPI ports, and the host port. The external port consists of an 8- or 16-bit data bus, a 22-bit address bus, and control signals. The data bus is configurable to provide an 8- or 16-bit